



Group Art Unit: 2815
Examiner: Edward Joseph Wojciechowicz

TFW
3.2815

In Re PATENT APPLICATION of:

Applicant(s): Katsuhiro HISAKA

Serial No.: 10/673,392

Filing Date: September 30, 2003

For: FUSE CIRCUIT AND SEMICONDUCTOR
DEVICE INCLUDING THE SAME

Atty. Ref.: IIZ 138

)
)
) **PETITION FOR**
) **ONE-MONTH**
) **EXTENSION**
) **OF TIME**

April 13, 2006

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

It is respectfully requested that the period for replying to the Examiner's Action mailed on December 15, 2005, be extended for one month under the provisions of 37 C.F.R. 1.136(a).

The applicable extension fee of \$120.00 pursuant to 37 C.F.R. 1.17(a) is submitted herewith.

Should the remittance be accidentally missing or insufficient, the Commissioner is hereby authorized to charge the fee to our Deposit Account No. 18-0002, and advise us accordingly.

Respectfully submitted,

April 13, 2006
Date

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RHB/vm